

Notice of References Cited	Application/Control No. 10/812,003	Applicant(s)/Patent Under Reexamination KU ET AL.	
	Examiner Cheung Lee	Art Unit 2812	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,458,702	10-2002	Aloni, Efraim	438/682
	C	US-6,329,276	12-2001	Ku et al.	438/586
	D	US-2004/0123922	07-2004	Cabral et al.	148/527
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Stanley; "Silicon Processing for the VLSI ERA" Volume 2: Process Integration; 1990 by Lattice Press.; Pages 104-105
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.